

HONDA et al. Q64974
 DRIVING METHOD OF PLASMA DISPLAY
 PANEL
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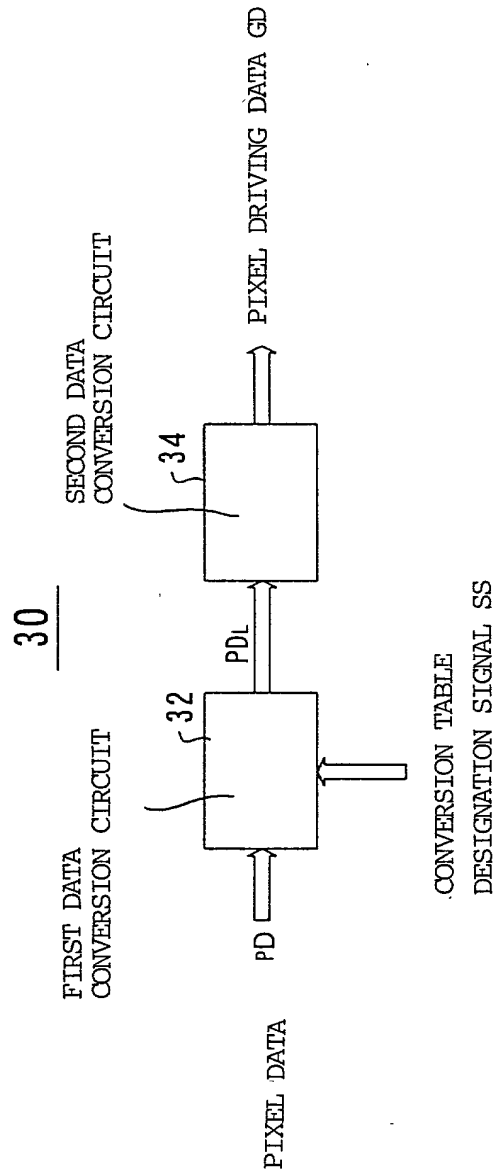


FIG. 2

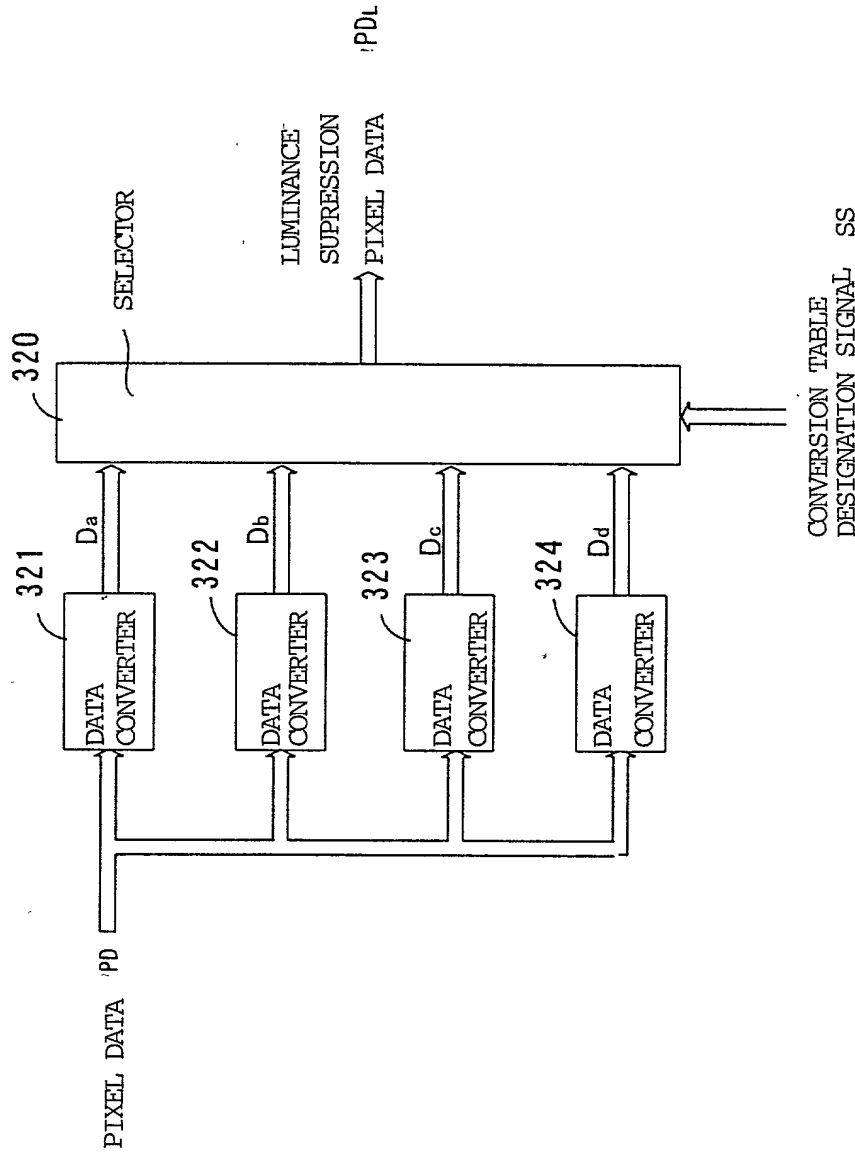


FIG. 3

FD5T90" 844E8860

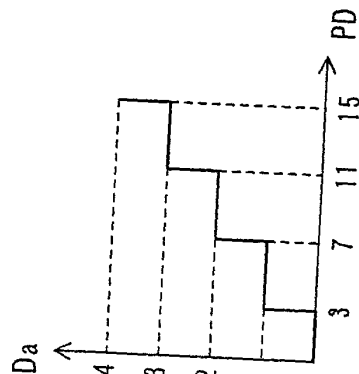


FIG. 4A

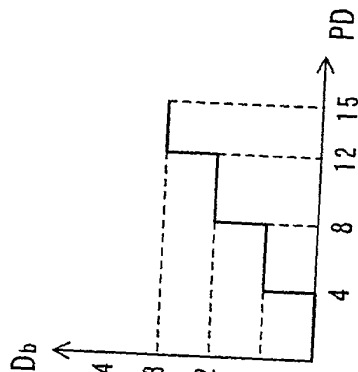


FIG. 4B

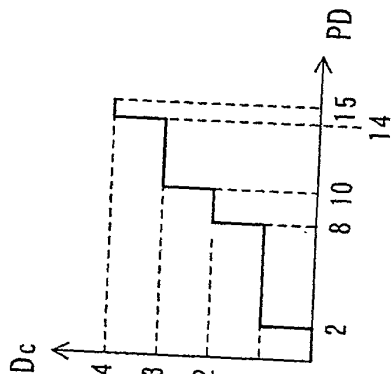


FIG. 4C

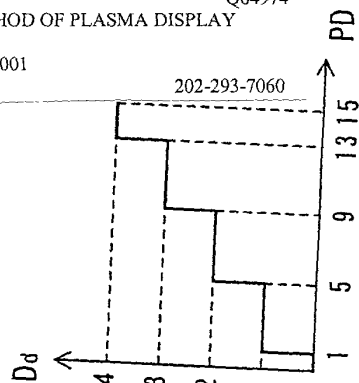


FIG. 4D

CONVERSION TABLE OF
 SECOND DATA CONVERSION
 CIRCUIT 34

[SELECTIVE ERASE] LIGHT EMISSION
 DRIVING PATTERN

GRADATION						SF				LUMINANCE			
	PD _L	GD				1	2	3	4	A	B	C	D
1	000	1	1	1	1	●	●	●	●	0	0	0	0
2	001	0	1	1	1	○	●	●	●	20	28	12	4
3	010	0	0	1	1	○	○	●	●	72	88	56	40
4	011	0	0	0	1	○	○	○	●	156	180	132	108
5	100	0	0	0	0	○	○	○	○	272	240	208	

BLACK CIRCLE:SELECTIVE ERASE DISCHARGE (LIGHT, NON-EMISSION)

WHITE CIRCLE:SUSTAIN DISCHARGE(LIGHT EMISSION)

FIG. 5



FIG. 6.

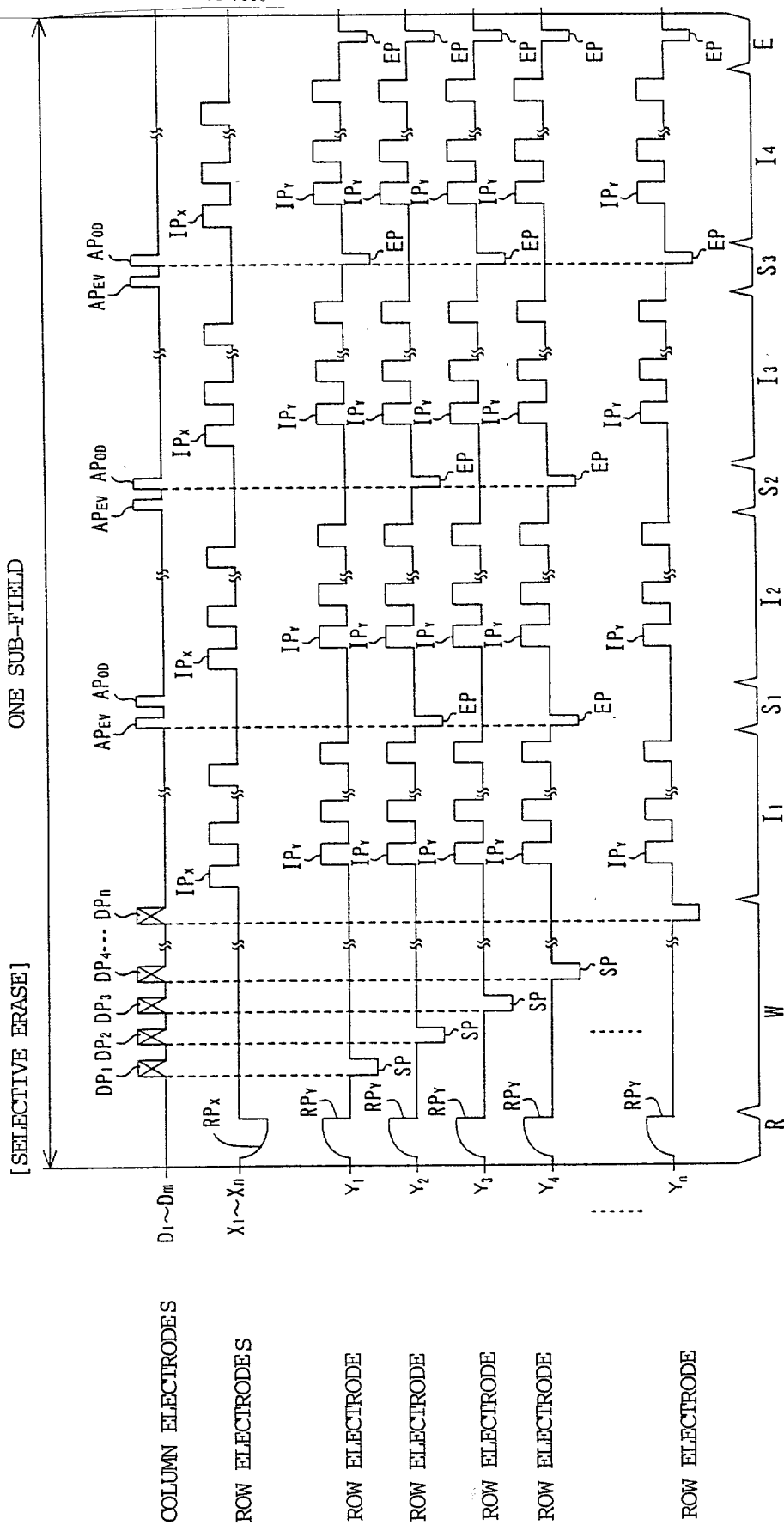


FIG. 7

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	1	2	3	4(COLUMN)
1	A	B	A	B	
2	C	D	C	D	
3	A	B	A	B	
4	C	D	C	D	
...					
(\ROW)					

FIG. 9

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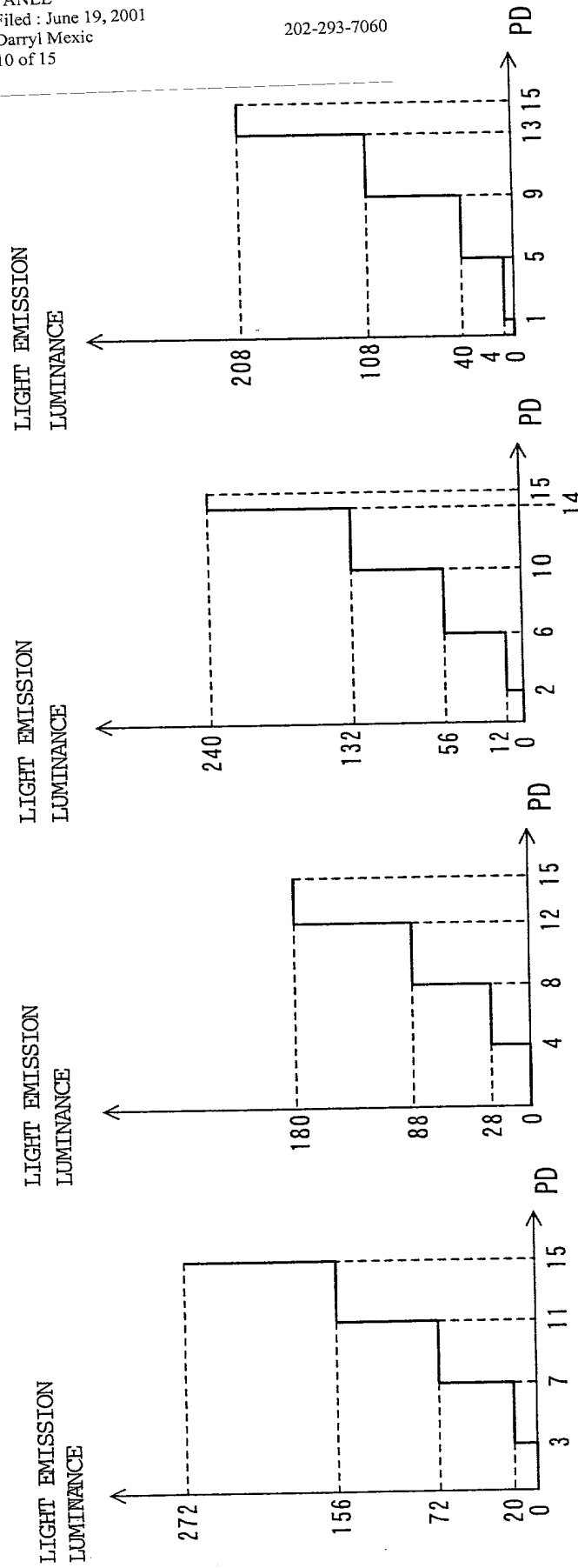


FIG. 10A FIG. 10B FIG. 10C FIG. 10D

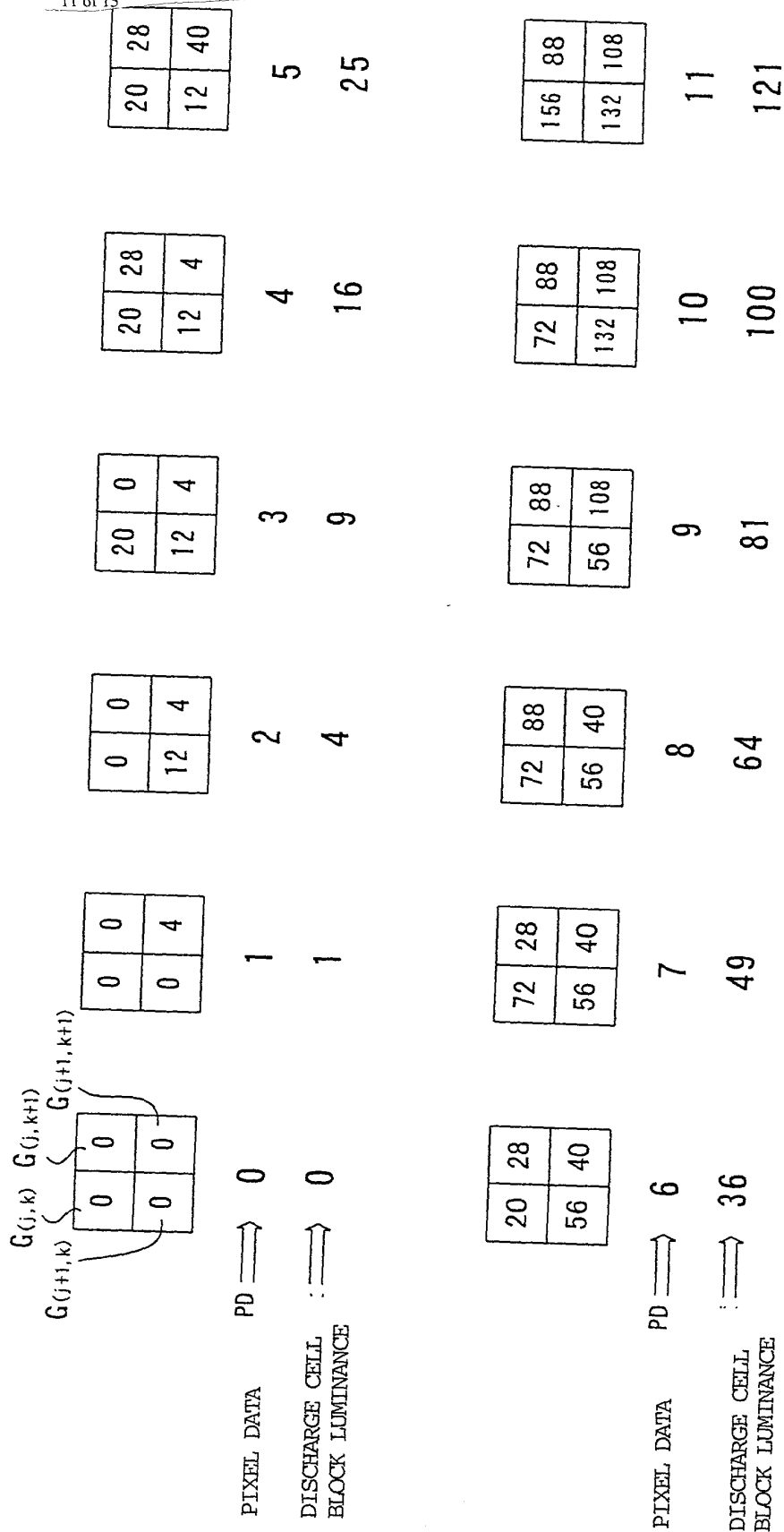
[illegible]

FIG. 11

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LIGHT EMISSION
LUMINANCE

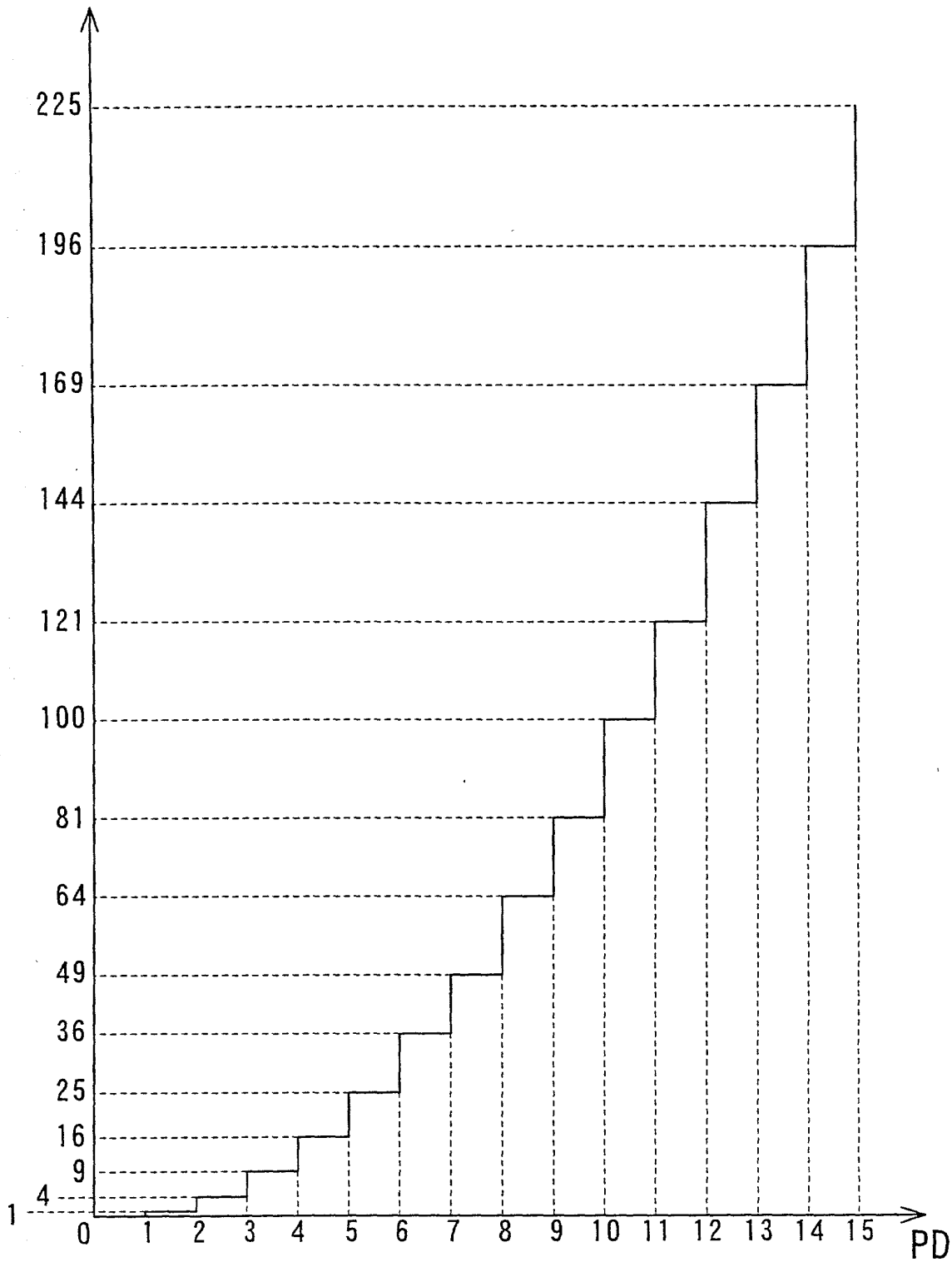


FIG. 12

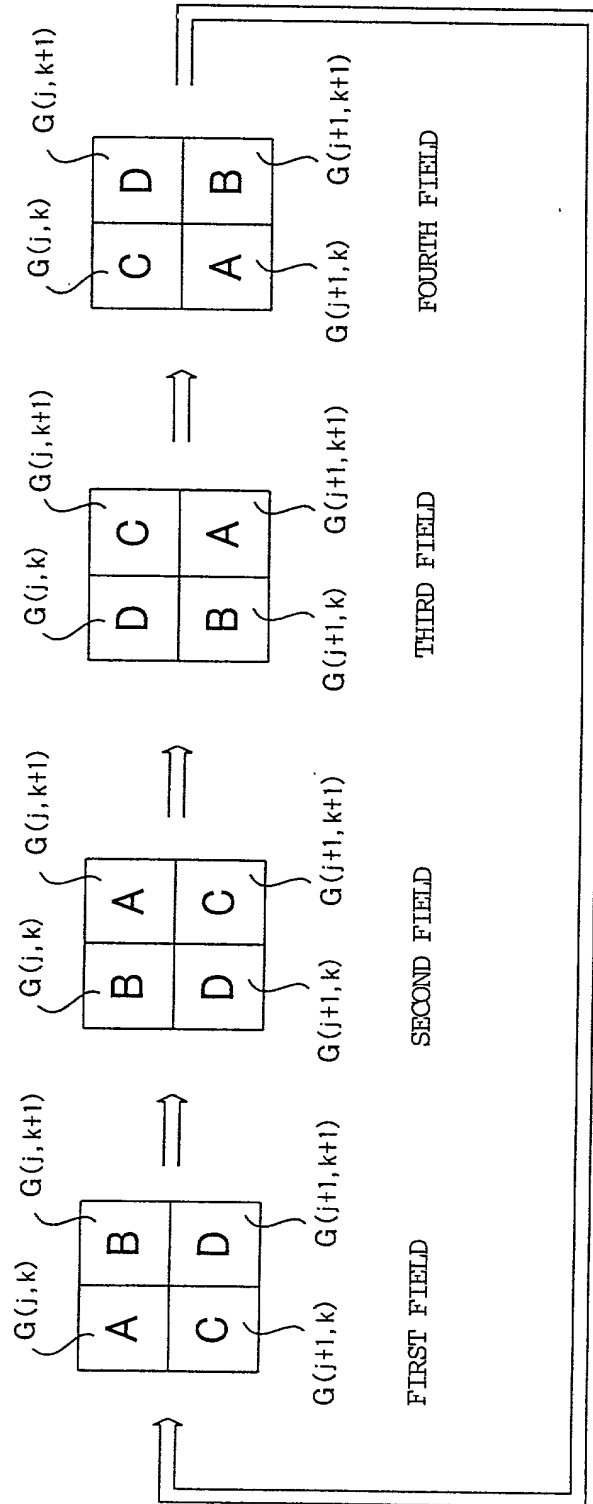


FIG. 13

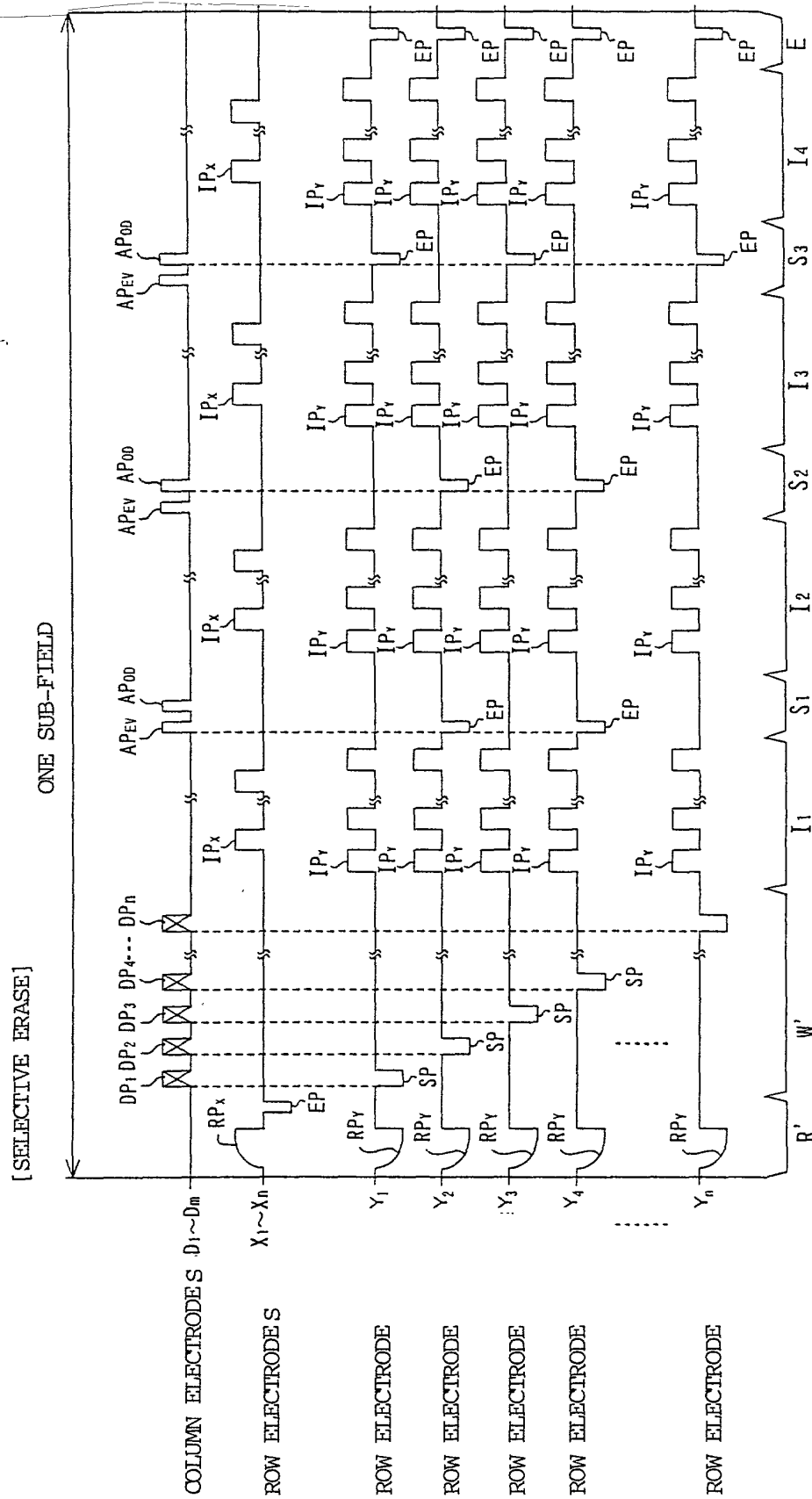


FIG. 14

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CONVERSION TABLE OF
SECOND DATA CONVERSION
CIRCUIT 34

[SELECTIVE WRITE] LIGHT EMISSION
DRIVING PATTERN

GRADATION	PD _L				GD				SF				LUMINANCE			
	1	2	3	4	1	2	3	4	1	2	3	4	A	B	C	D
1	000	0	0	0	0	0	0	0					0	0	0	0
2	001	1	0	0	0	0	0	0	⊙				20	28	12	4
3	010	1	1	0	0	0	0	0	⊙	⊙			72	88	56	40
4	011	1	1	1	0	0	0	0	⊙	⊙	⊙		156	180	132	108
5	100	1	1	1	1	1	1	1	⊙	⊙	⊙	⊙	272		240	208

FIG. 15